

Amendments to the Claims

Claims 1 and 2 (**Canceled**)

Claim 3 (**Currently Amended**) An image processor comprising:

a first storage unit operable to store separated image data formed by several pieces of image pixel data, the separated image data having a predetermined width and being separated from several lines of image data;

a data readout unit operable to sequentially read out the several pieces of image pixel data stored by said first storage unit;

a filtering unit operable to filter target image pixel data subject to filtering using predetermined pieces of the image pixel data that are read out from said data readout ~~unit~~ unit, thereby outputting filtered pixel data and non-filtered pixel data;

~~a second storage unit operable to store the filtered target image data subject to filtering;~~
and

~~—— a data write unit operable to write the filtered target image data subject to filtering to said second storage unit~~

~~—— a setting unit operable to set a mode signal indicating whether or not the non-filtered pixel data is to be fed; and~~

~~—— a data output control unit operable to control feeding of the non-filtered pixel data according to the mode signal,~~

~~—— wherein said first storage unit has a same width as the predetermined width.~~

Claim 4 (**Currently Amended**) An image processor as defined in claim 3, wherein ~~said filtering unit comprises a data output control unit operable to feed the filtered target image data subject to filtering, but operable not to feed a predetermined piece of non-filtered~~ the mode signal divides the non-filtered pixel data into first non-filtered pixel data to be fed and second non-filtered pixel data not to be fed ~~image data in accordance with mode information.~~

Claim 5 (**Currently Amended**) An image processor as defined in claim 3, wherein said ~~filtering unit comprises a data output control unit~~ is operable to provide a countdown from an initial value each time when the image data is fed, the initial value being determined in accordance with the number of pixels to be processed, said data output control unit being operable to feed only a predetermined piece of non filtered image data as well as filtered target image data subject to filtering in accordance with results from the countdown feed the filtered pixel data and the non-filtered pixel data according to a timing generated by countdown from an initial value.

Claim 6 (**Currently Amended**) An image processor as defined in claim 3, wherein said filtering unit comprises ~~a~~ an image data-retaining unit operable to retain ~~several~~ a number of pieces of ~~image pixel data for use at the time of~~ to be used in a next filtering operation, ~~the several number of pieces of image pixel data for use at the time of~~ to be used in the next filtering operation being selected from among the several pieces of ~~image pixel~~ data read out by said data readout unit from said first storage unit.